Designing Optimal Compact Oblivious Routing for Datacenter Networks in Polynomial Time

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Abstract-Recent datacenter network topologies are shifting towards heterogeneous and structured topologies for high throughput, low cost, and simple manageability. However, they rely on sub-optimal routing approaches that fail to achieve their designed capacity. This paper proposes a process for designing optimal oblivious routing that is programmed compactly on programmable switches. The process consists of three contributions in tandem. We first transform a robust optimization problem for designing oblivious routing into a linear program, which is solvable in polynomial time but cannot scale for datacenter topologies. We then prove that the repeated structures in a datacenter topology lead to a structured optimal solution. We use this insight to formulate a scalable linear program, so an optimal oblivious routing solution is obtained in polynomial time for large-scale topologies. For real-world deployment, the optimal solution is converted into forwarding rules for programmable switches with stringent memory. With this constraint, we utilize the repeated structures in the optimal solution to group the forwarding rules, resulting in compact forwarding rules with a much smaller memory requirement. Extensive evaluations show our process i) obtains optimal solutions faster and more scalable than a state-of-the-art technique and ii) reduces the memory requirement by no less than 90% for most considered topologies.

I. INTRODUCTION

The design of network topologies for data centers is currently shifting towards heterogeneous, structured topologies for high capacity, low cost, and low manageability [1], [2]. This attempts to replace the folded-Clos family, including FatTree [3], Google's Jupiter [4], Facebook's Fabric [5], and Microsoft's VL2 [6]. Alternative topologies have been proposed, such as Xpander [7], FatClique [2], DRing [8], SlimFly [9], and other server-centric networks, where servers have routing capability, such as BCube [10] and DCell [11]. However, some of them [2], [7], [8], [11] rely on sub-optimal routing approaches, while the others [9], [10] use dynamic routing that requires specialized hardware. This leads to an active research problem of how to design oblivious routing that achieves designed capacity without specialized hardware for these and future topologies [12].

The oblivious routing approach is widely used in production networks because of its robustness and simplicity. The routes from every source to every destination are designed to be robust to traffic variation, so neither re-configuration



Fig. 1. The design process of optimal compact oblivious routing.

nor dynamic routing is required when traffic changes. For example, the folded-Clos family often employs the Equal-Cost Multi Paths (ECMP) routing approach [13] that splits traffic equally across all equal-cost paths, whose cost is independent of traffic. Another oblivious routing approach is Valiant Load Balancing (VLB) [14], [15], in which a traffic from a source is split equally to a set of intermediate switches, each then routes the received traffic to a destination. While, the ECMP and VLB approaches are optimal respectively for the folded-Clos and clique networks, they are sub-optimal for the alternative topologies due to topological differences.

Generally, designing an optimal oblivious routing solution for an arbitrary network topology is equivalent to solving a robust optimization problem [12], [15]–[17]. The work in [16] views this problem as a game and derived a linear program for intra-domain networks, albeit small-scale networks in comparison to datacenter networks. The recent work in [12] exploits the repeated network structures to reduce the complexity of a robust optimization problem, so an optimal oblivious routing solution is obtained for larger network sizes. In short, the first work can obtain the optimal routing solution in polynomial time (in the size of input instance, which is extremely large for large-scale networks), while the second work can scale to larger networks but could take non-polynomial time due to the complexity of robust optimization. This beg an open question: Could we design optimal oblivious routing in polynomial time that also scales for large datacenter networks to achieve the best of both worlds?

The memory constraint is another important issue for the real-world deployment of the oblivious routing. After an optimal routing solution is obtained, it is converted to forwarding rules that determine how traffic is split at each switch in a network. These rules are stored on switches with limited memory capacity, which becomes an issue for largescale datacenter networks with thousands of destinations. The previous work in [18] trade-off split accuracy with memory

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requirement, so the rules can fit available memory. This beg another question: *Could we reduce the memory requirement* given an optimal oblivious routing solution?

In this paper, we propose a process for designing optimal compact oblivious routing for datacenter networks to address the two challenges above, as shown in Figure 1. We first formulate an oblivious routing problem as robust optimization for general datacenter networks, including those alternative topologies to the folded-Clos family. We then transform the robust optimization into a linear program using decomposition and duality techniques. This reduces the complexity of optimal oblivious routing design to just solving a linear program that is polynomial-time solvable and is tractable for small-scale datacenter networks. Since datacenter networks usually have repeated structures, we show the existence of an automorphism-invariant optimal solution, whose variables have repeated values. From this insight, we formulate a scalable linear program that can scale to larger network sizes. As a result, we can design optimal oblivious routing for largescale datacenter networks with polynomial-time complexity. which addresses the first challenge. For the second challenge, we exploit the structure of the optimal oblivious routing solution, obtained from the scalable linear program, to group the forwarding rules. This grouping compacts the forwarding rules and significantly reduces memory requirement, which addresses the second challenge.

Extensive evaluations of the scalable linear program on various datacenter network topologies and sizes show that the linear program is faster and more scalable than the state-ofthe-art technique in [12] and another linear formulation in [16]. Moreover, the forwarding rules grouping significantly reduces forwarding rules' memory requirement by no less than 90% for the majority of considered topologies. We also provide possible applications of the optimal compact oblivious routing.

The contributions of this work are summarized as follows:

- We transform a robust optimization problem for designing oblivious routing into a linear program, so an optimal oblivious routing solution is obtained in polynomial time for small-scale topologies.
- We prove that a datacenter network topology with repeated structures has an optimal solution whose variables also have repetition. We use this insight to formulate a scalable linear program that is solvable in polynomial time and is tractable for larger topologies.
- We utilize the structure of the optimal solution of the scalable linear program, to compact forwarding rules and reduce memory requirement for real-world deployment.

Besides, we release our code to the research community.

This paper is organized as follows. Section II describes the model of datacenter networks and the oblivious routing problem. Section III transforms the problem into a linear program. The repeated network structures are exploited to improve scalability in Section IV. Section V describes a grouping technique to compact forwarding rules for real-world deployment. Extensive evaluations are provided in Section VI before the conclusion in Section VII.



Fig. 2. A simple network consists of routing-capable nodes, routing-incapable nodes, and links. These nodes and links could have different capacities.

II. SYSTEM MODEL

We formally model datacenter networks, traffic demands, and routing components before formulating an oblivious routing optimization problem.

A. Network model

Our network model generalizes the previous model in [12] to capture both the traditional server-switch architecture [1], [2], [7]–[9], [19], [20] and the server-centric architecture [10], [11]. The differences between these architectures are that every server in the former has only one direct connection to a switch and has no routing capability, while every server in the latter has the routing capability and may have multiple direct connections to other servers or switches. To deal with these differences, we model a datacenter network by a graph with two types of nodes: routing-capable nodes and routing-incapable nodes, as shown in Figure 2. Let S and \mathcal{P} be the sets of routing-capable nodes and routing-incapable nodes respectively. The set of all nodes is denoted by $\mathcal{N} = S \cup \mathcal{P}$.

Nodes are interconnected by links. Node *i* and node *j* are connected by link (i, j) with capacity C_{ij} . We denote the set of all links by \mathcal{L} . Every link is bi-directional, so $(i, j) \in \mathcal{L}$ if and only if $(j, i) \in \mathcal{L}$ and $C_{ij} = C_{ji}$ for every $(i, j) \in \mathcal{L}$.

B. Traffic model

Node n has capacity H_n to independently send and receive traffic for every $n \in \mathcal{N}$. We denote the set of nodes that have positive capacities by $\mathcal{H} = \{n \in \mathcal{N} : H_n > 0\}$. A commodity (u, v) is a pair of source node u and destination node v where both nodes have positive capacity. We denote the set of all commodities by $\mathcal{C} = \{(u, v) \in \mathcal{H}^2 : u \neq v\}$.

Node u sends traffic to node v and creates a traffic demand for commodity (u, v). This traffic demand is denoted by t^{uv} . The traffic demands from every commodity form a traffic matrix $[t^{uv}] \in \mathbb{R}^{|\mathcal{C}|}_+$, where \mathbb{R}_+ is a set of non-negative reals.

Since traffic matrices inside a datacenter network could be unpredictable, we consider the set of all possible traffic matrices [12], [15], [21]:

$$\mathcal{T} = \left\{ \begin{array}{l} \sum_{v \in \mathcal{H}_{-u}} t^{uv} \leq H_u &, \forall u \in \mathcal{H} \\ \sum_{u \in \mathcal{H}_{-v}} t^{uv} \leq H_v &, \forall v \in \mathcal{H} \\ t^{uv} \in \mathbb{R}_+ &, \forall (u,v) \in \mathcal{C} \end{array} \right\}, \quad (1)$$

where A_{-x} contains all members of A but excludes x, i.e., $A_{-x} = A \setminus \{x\}$. The first constraint in (1) limits the amount of traffic generated from each node by the node's capacity. Similarly, the second constraint bounds the amount of traffic each node can receive by the node's capacity.

C. Routing model

We adopt the multi-commodity flow model with the commodity set C. For every commodity $(u, v) \in C$, the share of commodity's demand over link (i, j) is denoted by f_{ij}^{uv} for every link $(i, j) \in \mathcal{L}$. Notice that, given a traffic demand t^{uv} , the amount of the demand over link (i, j) is $t^{uv}f_{ij}^{uv}$. These shares over all links in the network, $\{f_{ij}^{uv}: (i, j) \in \mathcal{L}\}$, form the shares of commodity (u, v). We denote the shares of all commodities by $[f_{ij}^{uv}]$, where $[f_{ij}^{uv}] \in \mathbb{R}^{|\mathcal{L}| \times |\mathcal{L}|}_+$.

Recall that there are two node types. A routing-capable node can route every traffic independent of the traffic's commodity. A routing-incapable node can only route traffic corresponding to the commodities involving the node, i.e., the node is either a source or a destination of the traffic. Define $\mathbb{I}[x]$ as an indicator function that returns 1 if the statement x is true; otherwise 0. We define the set of all possible shares as follows:

$$\mathcal{F} = \left\{ \begin{array}{l} \sum\limits_{j \in \mathcal{O}(i)} f_{ij}^{uv} - \sum\limits_{j \in \mathcal{I}(i)} f_{ji}^{uv} = \mathbb{I}[i = u] - \mathbb{I}[i = v] \\ , \forall (u, v) \in \mathcal{C}, \forall i \in \mathcal{N} \\ f_{ij}^{uv} = 0 &, \forall (u, v) \in \mathcal{C}, \forall (i, j) \in \mathcal{N} \times \mathcal{P}_{-v} \\ f_{iu}^{uv} = f_{vi}^{uv} = 0, \forall (u, v) \in \mathcal{C}, \forall i \in \mathcal{N} \\ f_{ij}^{uv} \in \mathbb{R}_{+} &, \forall (u, v) \in \mathcal{C}, \forall (i, j) \in \mathcal{L} \end{array} \right\},$$

$$(2)$$

where $\mathcal{O}(i)$ and $\mathcal{I}(i)$ are respectively the set of nodes corresponding to the outgoing links from node *i* and the set of nodes corresponding to the incoming links to node *i*. The first constraint is the share conservation where the total share at every source and every destination is one. The second constraint enforces that every routing-incapable node does not involve in the routes of other commodities unrelated to the node. The third constraint ensures that both source and destination are the endpoints of their corresponding traffic.

D. Oblivious routing formulation

Given shares $[f_{ij}^{uv}]$ and a traffic matrix $[t^{uv}]$, we define the congestion ratio of link (x, y) by the proportion of total traffic demand on the link to the link's capacity [16], [17]:

$$\sum_{(u,v)\in\mathcal{C}}\frac{t^{uv}f^{uv}_{xy}}{C_{xy}}$$

An oblivious routing problem aims to design the shares $[f_{ij}^{uv}]$ that minimizes this congestion ratio across every link in the network and under all possible traffic demands.¹ We formulate this oblivious routing problem as follows:

$$\min_{\substack{[f_{ij}^{uv}] \in \mathcal{F} \\ [t^{uv}] \in \mathcal{T}}} \max_{\substack{(x,y) \in \mathcal{L}, \\ [t^{uv}] \in \mathcal{T}}} \sum_{(u,v) \in \mathcal{C}} \frac{t^{uv} f^{uv}_{xy}}{C_{xy}}.$$
 (3)

This formulation has two challenges: the robust objective with infinite possibilities of traffic demands and the scalability of the formulation for large networks. The first challenge stems

¹This is equivalent to the performance ratio in [16], [17] when the minimum possible maximum link utilization is ignored.

from the fact that the congestion ratio is a function of every traffic demand in the traffic set \mathcal{T} , so the entire set must be considered. The second challenge is due to the sizes of datacenter networks, resulting in large numbers of decision variables and constraints in the formulation. We tackle the two challenges successively in Section III and Section IV.

III. LINEAR FORMULATION

This section tackles the challenge of the robust objective by transforming the robust formation in (3) to a simpler linear program, so an optimal oblivious routing solution can be designed for appropriate network sizes. This transformation is inspired by [16], which considers a different network model without the traffic set and uses different proof techniques.

We first observe that each link has its worst-cast traffic matrix, yielding its maximal congestion ratio. Therefore, we introduce traffic matrix $[t_{xy}^{uv}] \in \mathcal{T}$ for every link (x, y) to decouple the inner maximization in (3) as follows:

$$\max_{\substack{(x,y)\in\mathcal{L},\\[t^{uv}]\in\mathcal{T}}}\sum_{(u,v)\in\mathcal{C}}\frac{t^{uv}f_{xy}^{uv}}{C_{xy}} = \max_{(x,y)\in\mathcal{L}}\max_{[t^{uv}_{xy}]\in\mathcal{T}}\sum_{(u,v)\in\mathcal{C}}\frac{t^{uv}_{xy}f_{xy}^{uv}}{C_{xy}}$$
(4)

The above decomposition allows us to consider the subproblem, $\max_{[t_{xy}^{uv}]\in\mathcal{T}}\sum_{(u,v)\in\mathcal{C}}\frac{t_{xy}^{uv}f_{xy}^{uv}}{C_{xy}}$, which finds the worstcase congestion ratio of link (x, y), in isolation. Given a considered link (x, y) and a share solution $[f_{ij}^{uv}]$, this subproblem is rewritten as the following formulation:

Maximize
$$\sum_{(u,v)\in\mathcal{C}} \frac{t_{xy}^{uv} f_{xy}^{uv}}{C_{xy}}$$

Subject to
$$\sum_{v\in\mathcal{H}_{-u}} t_{xy}^{uv} \leq H_u \quad , \forall u \in \mathcal{H}$$
$$\sum_{u\in\mathcal{H}_{-v}} t_{xy}^{uv} \leq H_v \quad , \forall v \in \mathcal{H}$$
$$t_{xy}^{uv} \in \mathbb{R}_+ \quad , \forall (u,v) \in \mathcal{C}.$$

Instead of solving this sub-problem directly, we consider its dual problem with dual variables $[\beta_{xy}^u]_{u \in \mathcal{H}}$ and $[\gamma_{xy}^u]_{u \in \mathcal{H}}$:

Minimize
$$\sum_{u \in \mathcal{H}} H_u \left(\beta_{xy}^u + \gamma_{xy}^u \right)$$

Subject to
$$\frac{f_{xy}^{uv}}{C_{xy}} - \beta_{xy}^u - \gamma_{xy}^v \le 0 \quad , \forall (u,v) \in \mathcal{C} \qquad (6)$$
$$\beta_{xy}^u \in \mathbb{R}_+, \ \gamma_{xy}^u \in \mathbb{R}_+ \quad , \forall u \in \mathcal{H}.$$

Lemma 1. For a given link (x, y) and a share solution $[f_{ij}^{uv}]$, the formulation in (6) is the dual of the problem in (5).

Proof: Fix a link (x, y) and a share solution $[f_{ij}^{uv}]$. We derive a dual problem by the duality technique [22]. Define non-negative dual variables $[\beta_{xy}^u]_{u \in \mathcal{H}}$, $[\gamma_{xy}^u]_{u \in \mathcal{H}}$ respectively

for the first two inequality constraints of the sub-problem in (5). We define the Lagrangian associated with this problem as

$$L\left(\left[t_{xy}^{uv}\right], \left[\beta_{xy}^{u}\right], \left[\gamma_{xy}^{v}\right]\right) = \sum_{(u,v)\in\mathcal{C}} \frac{t_{xy}^{uv} f_{xy}^{uv}}{C_{xy}}$$
$$-\sum_{u\in\mathcal{H}} \beta_{xy}^{u} \left(\sum_{v\in\mathcal{H}_{-u}} t_{xy}^{uv} - H_{u}\right) - \sum_{v\in\mathcal{H}} \gamma_{xy}^{v} \left(\sum_{u\in\mathcal{H}_{-v}} t_{xy}^{uv} - H_{v}\right)$$
$$= \sum_{(u,v)\in\mathcal{C}} t_{xy}^{uv} \left(\frac{f_{xy}^{uv}}{C_{xy}} - \beta_{xy}^{u} - \gamma_{xy}^{v}\right) + \sum_{u\in\mathcal{H}} H_{u} \left(\beta_{xy}^{u} + \gamma_{xy}^{u}\right).$$

The dual function is defined as

$$D\left(\left[\beta_{xy}^{u}\right],\left[\gamma_{xy}^{v}\right]\right) = \max_{\left[t_{xy}^{uv}\right] \in \mathbb{R}_{+}^{|\mathcal{C}|}} L\left(\left[t_{xy}^{uv}\right],\left[\beta_{xy}^{u}\right],\left[\gamma_{xy}^{v}\right]\right).$$

Note that this dual function is undefined, $D\left(\left[\beta_{xy}^{u}\right], \left[\gamma_{xy}^{v}\right]\right) \rightarrow \infty$ when $\frac{f_{xy}^{uv}}{C_{xy}} - \beta_{xy}^{u} - \gamma_{xy}^{v} > 0$ for some commodity $(u, v) \in \mathcal{C}$. We only consider the well-defined dual function $D\left(\left[\beta_{xy}^{u}\right],\left[\gamma_{xy}^{v}\right]\right) = \sum_{u \in \mathcal{H}} H_{u}\left(\beta_{xy}^{u} + \gamma_{xy}^{u}\right) \text{ when } \frac{f_{xy}^{uv}}{C_{xy}} - \beta_{xy}^{u} - \gamma_{xy}^{v} \leq 0 \text{ for every commodity } (u,v) \in \mathcal{C}. \text{ This is because a dual problem will minimize this dual function, i.e., } \min_{\left[\beta_{xy}^{u}\right] \in \mathbb{R}_{+}^{|\mathcal{H}|},\left[\gamma_{xy}^{v}\right] \in \mathbb{R}_{+}^{|\mathcal{H}|} D\left(\left[\beta_{xy}^{u}\right],\left[\gamma_{xy}^{v}\right]\right), \text{ resulting in the dual problem in (6), which proves the lemma.}$

We use the dual problem in (6) to transform the robust optimization problem in (3) into the following linear program:

Minimize η

Subject to
$$\sum_{u \in \mathcal{H}} H_u \left(\beta_{ij}^u + \gamma_{ij}^u \right) \leq \eta \quad , \forall (i,j) \in \mathcal{L}$$
$$\frac{f_{ij}^{uv}}{C_{ij}} - \beta_{ij}^u - \gamma_{ij}^v \leq 0 \quad , \forall (u,v) \in \mathcal{C}, \forall (i,j) \in \mathcal{L}$$
$$\beta_{ij}^u \in \mathbb{R}_+, \ \gamma_{ij}^u \in \mathbb{R}_+ \quad , \forall u \in \mathcal{H}, \forall (i,j) \in \mathcal{L}$$
$$[f_{ij}^{uv}] \in \mathcal{F}.$$
(7)

Theorem 1. The robust oblivious routing formulation in (3) and the linear program in (7) are equivalent.

Proof: The proof replaces the sub-problem in (5) with the dual problem in (6) on the right-hand side of equation (4). Let $g_{xy}([f_{ij}^{uv}])$ represent the dual problem in (6) of link (x, y)given routing variables $[f_{ij}^{uv}]$. The replacement yields:

$$\max_{\substack{(i,j)\in\mathcal{L},\\[t^{uv}]\in\mathcal{T}}}\sum_{(u,v)\in\mathcal{C}}\frac{t^{uv}f_{ij}^{uv}}{C_{ij}} = \max_{(x,y)\in\mathcal{L}}g_{xy}([f_{ij}^{uv}]).$$

Since the left-hand side of the above equation is the inner maximization of the oblivious routing formulation in (3), it follows that the formulation is equivalent to

$$\min_{\substack{[f_{ij}^{uv}] \in \mathcal{F} \quad (x,y) \in \mathcal{L}}} \max_{g_{xy}} g_{xy}([f_{ij}^{uv}])$$

The above min-max problem can be transformed into a linear program with an auxiliary variable η as follows:

Minimize
$$\eta$$

Subject to $g_{xy}([f_{ij}^{uv}]) \leq \eta$, $\forall (x,y) \in \mathcal{L}$
 $[f_{ij}^{uv}] \in \mathcal{F}.$



Fig. 3. Automorphism example. Nodes in the right network are nodes in the left network relabeled by the automorphism $\phi(n) = n - 3 \mod 12$. Both networks have the same adjacency, link capacity distribution, node types, and

Finally, replacing the dual problem $g_{xy}([f_{ij}^{uv}])$ with its objective function and constraints in (6) gives the linear program in (7) and proves the theorem.

node capacity distribution.

The implication of Theorem 1 is that an optimal oblivious routing solution of the robust formulation in (3) can be obtained by solving the linear program in (7). Since linear programming is solvable in polynomial time [23], [24], we can design optimal oblivious routing in polynomial time for appropriate network sizes, in terms of variables and constraints. However, the scales of datacenter networks could render the linear program in (7) intractable in practice. The next section tackles this scalability issue.

IV. SCALABLE LINEAR FORMULATION

This section tackles the scalability of the linear formulation in (7) for large-scale datacenter networks. We exploit the repeated structures in a network topology to scale the formulation. This idea is inspired by the work in [12], which is non-polynomial time and has no routing-incapable nodes.

A. Automorphism in datacenter networks

The repeated structures in a datacenter network is captured by graph automorphism. Graph automorphism is a mapping of nodes in a graph to the same set of nodes in the same graph such that the new graph with the mapped nodes is similar to the original graph [25]. In particular, the described automorphism preserves node adjacency. We extend this automorphism to our network model by the definition of our automorphism.

Definition 1. A mapping function $\phi : \mathcal{N} \to \mathcal{N}$ is an automorphism if the following conditions hold:

- 1) Adjacency: $(\phi(i), \phi(j)) \in \mathcal{L}, \forall (i, j) \in \mathcal{L}.$
- 2) Link capacity: $C_{\phi(i)\phi(j)} = C_{ij}, \forall (i, j) \in \mathcal{L}.$ 3) Type: $\phi(n) \in \mathcal{S}, \forall n \in \mathcal{S} \text{ and } \phi(n) \in \mathcal{P}, \forall n \in \mathcal{P}.$
- 4) Node capacity: $H_{\phi(n)} = H_n, \forall n \in \mathcal{N}.$

The automorphism in Definition 1 is illustrated in Figure 3. The first condition ensures that the adjacency between nodes are preserved under the automorphism. The second condition keeps the capacity of every link the same after the automorphism mapping. The type and capacity of every node also stay the same under the automorphism from the last two conditions.

Let Φ be the set of all automorphisms satisfying Definition 1. Since the numbers of nodes and links in a network are



Fig. 4. A graph with colored vertices is constructed by Algorithm 1 from the simple network in Figure 2.

finite, there are finite permutations, and the cardinality of the set is finite, $|\Phi| < \infty$. In addition, we define $\hat{\Phi}$ as the set of generators that entirely generate the automorphism set Φ . This generator set can be obtained from off-the-shelf software, such as nauty [26].

Algorithm 1: Graph construction for generator set $\hat{\Phi}$

To obtain the generator set $\hat{\Phi}$, we encode the four properties in Definition 1 on a network topology as a graph with colored vertices by Algorithm 1. Figure 4 shows the graph constructed from the network in Figure 2 by the algorithm. The algorithm constructs a graph with edges and colored vertices from a network topology as follows. Vertices associated with nodes having the same type and node capacity are assigned the same color. Any two vertices associated with nodes having different types or different capacities are given different colors. This construction ensures the preservation of node type and node capacity in the generator set. For links in the network, each link yields a new vertex with two edges, each connects to the vertex associated with each side of the link. Vertices associated with links having the same capacity are assigned the same color. Any two vertices associated with links having different capacities are given different colors. This construction ensures the properties on adjacency and link capacity are enforced.

We use the automorphism set Φ and its generator set $\tilde{\Phi}$ to identify a special optimal oblivious routing solution of the linear program in (7) in the next section.

B. Automorphism-invariant optimal solution

In this section, we show that the repeated structures in a datacenter network lead to an optimal oblivious routing solution that also has repeated structures.

We first argue that the linear program in (7) has an optimal solution. We know that the robust optimization in (3) aims to minimize the congestion ratio over the traffic set, which is assumed to be nonempty, and the sum of all link capacities in the network is finite. Therefore, the minimal congestion ratio exists. Since the linear program in (7) is equivalent to the robust optimization in (3) by Theorem 1, it must have an optimal solution. Let $(\eta^*, [\beta_{ij}^{u*}], [\gamma_{ij}^{u*}])$ be an optimal solution of the linear program in (7). We then show that there is another optimal solution that can be constructed from an automorphism.

Lemma 2. A solution $(\eta, [\beta_{ij}^u], [\gamma_{ij}^u], [f_{ij}^{uv}])$ is an optimal solution of the linear program in (7) given any $\phi \in \Phi$ and

$$\eta = \eta^*, \ \beta^u_{ij} = \beta^{\phi(u)*}_{\phi(i)\phi(j)}, \ \gamma^u_{ij} = \gamma^{\phi(u)*}_{\phi(i)\phi(j)}, \ f^{uv}_{ij} = f^{\phi(u)\phi(v)*}_{\phi(i)\phi(j)}.$$

1

Proof: Fix an automorphism $\phi \in \Phi$. Since $\eta = \eta^*$, the objective cost under the new solution equals the optimal objective cost. We then need to show that the solution, constructed from the automorphism, is feasible and satisfies all constraints in (7) and (2). We begin with the domains of variables β_{ij}^u and γ_{ij}^u in (7) and variables f_{ij}^{uv} in (2). These domains are non-negative reals, so the variables of the constructed solution are in the same domains. Next, we consider the constraints in (7).

Considering the first constraint in (7) with link (i, j), we have that

$$\sum_{u \in \mathcal{H}} H_u \left(\beta_{ij}^u + \gamma_{ij}^u \right) = \sum_{u \in \mathcal{H}} H_{\phi(u)} \left(\beta_{\phi(i)\phi(j)}^{\phi(u)*} + \gamma_{\phi(i)\phi(j)}^{\phi(u)*} \right)$$
$$= \sum_{u \in \mathcal{H}} H_u \left(\beta_{\phi(i)\phi(j)}^{u*} + \gamma_{\phi(i)\phi(j)}^{u*} \right) \le \eta^* = \eta.$$

The first equality substitutes the solution and uses the node capacity property in Definition 1. Reindexing $u \in \mathcal{H}$ leads to the second equality. The last inequality holds because the constraint associated with link $(\phi(i), \phi(j))$ holds under the optimal solution. Thus, the first constraint in (7) is satisfied.

Considering the second constraint in (7) with commodity (u, v) and link (i, j), we have that

$$\frac{f_{ij}^{uv}}{C_{ij}} - \beta_{ij}^{u} - \gamma_{ij}^{v} = \frac{f_{\phi(i)\phi(j)}^{\phi(u)\phi(v)*}}{C_{\phi(i)\phi(j)}} - \beta_{\phi(i)\phi(j)}^{\phi(u)*} - \gamma_{\phi(i)\phi(j)}^{\phi(v)*} \le 0.$$

The first equality substitutes the solution and uses the link capacity property in Definition 1. The last inequality holds because the constraint associated with commodity $(\phi(u), \phi(v))$ and link $(\phi(i), \phi(j))$ holds under the optimal solution. Thus, the second constraint in (7) is satisfied.

We then consider the constraints in (2). Considering the second constraint with commodity $(u, v) \in C$ and link $(i, j) \in \mathcal{N} \times \mathcal{P}_{-v}$, we have that $f_{ij}^{uv} = f_{\phi(i)\phi(j)}^{\phi(u)\phi(v)*}$. Since node j is routing-incapable and $j \neq v$, node $\phi(j)$ is also routing-incapable and $\phi(j) \neq \phi(v)$ from Definition 1. It follows

that $f_{\phi(i)\phi(j)}^{\phi(u)\phi(v)*} = 0$ because the optimal share of commodity $(\phi(u), \phi(v))$ never routes traffic over link $(\phi(i), \phi(j))$ as $\phi(j) \in \mathcal{P}_{-\phi(v)}$. Therefore, we have $f_{ij}^{uv} = 0$, and the second constraint in (2) is satisfied.

Considering the third constraint in (2) with commodity $(u, v) \in \mathcal{C}$ and node $i \in \mathcal{N}$, we have that $f_{iuv}^{uv} = f_{\phi(i)\phi(u)}^{\phi(u)\phi(v)*} = 0$ because the optimal share of commodity $(\phi(u), \phi(v))$ never routes traffic back to the traffic's source node. A similar argument can be applied to the constraint $f_{vi}^{uv} = 0$ as the optimal share of commodity $(\phi(u), \phi(v))$ never routes traffic away from the traffic's destination node. Therefore, The third constraint in (2) is satisfied. For the first constraint in (2), the proof that the constraint is satisfied is similar to the proof in [12] and is omitted for brevity. Thus, it holds that $[f_{ij}^{uv}] \in \mathcal{F}$.

Altogether, the solution yields the same optimal objective cost and satisfies all constraints of the linear program in (7). It must be an optimal solution.

The implication of Lemma 2 is that we can construct multiple optimal solutions from an optimal solution and the automorphism set. Next, we use these optimal solutions to identify an optimal solution with repetitive variables.

Theorem 2. There exists an automorphism-invariant solution $(\hat{\eta}, [\hat{\beta}_{ij}^{u}], [\hat{\gamma}_{ij}^{u}], [\hat{f}_{ij}^{uv}])$ that is optimal for the linear program in (7) and satisfies:

$$\hat{f}_{ij}^{uv} = \hat{f}_{\phi(i)\phi(j)}^{\phi(u)\phi(v)}, \ \hat{\beta}_{ij}^{u} = \hat{\beta}_{\phi(i)\phi(j)}^{\phi(u)}, \ \hat{\gamma}_{ij}^{u} = \hat{\gamma}_{\phi(i)\phi(j)}^{\phi(u)}, \ \forall \phi \in \Phi.$$

Proof: We first construct a solution before showing that it is optimal and satisfies the automorphism-invariant property. Let $(\hat{\eta}, [\hat{\beta}_{ij}^{u}], [\hat{\gamma}_{ij}^{u}], [\hat{f}_{ij}^{uv}])$ be a solution such that

$$\hat{\eta} = \eta^{*}, \qquad \hat{f}_{ij}^{uv} = \frac{1}{|\Phi|} \sum_{\phi \in \Phi} f_{\phi(i)\phi(j)}^{\phi(u)\phi(v)*}$$
$$\hat{\beta}_{ij}^{u} = \frac{1}{|\Phi|} \sum_{\phi \in \Phi} \beta_{\phi(i)\phi(j)}^{\phi(u)*}, \qquad \hat{\gamma}_{ij}^{u} = \frac{1}{|\Phi|} \sum_{\phi \in \Phi} \gamma_{\phi(i)\phi(j)}^{\phi(u)*},$$

where $(\eta^*, [\beta_{ij}^{u*}], [\gamma_{ij}^{u*}], [f_{ij}^{uv*}])$ is an existing optimal solution.

Since $\hat{\eta} = \eta^*$, the objective cost under the solution equals to the optimal cost. We then need to show that all constraints in (7) are satisfied.

The linearity in the construction of the solution implies that i) the domains of variables β_{ij}^u and γ_{ij}^u in (7), ii) the domains of variables f_{ij}^{uv} in (2), and iii) the second and third constraints in (2) are satisfied. The proof that the first constraint in (2) is satisfied is similar to the proof in [12] and is omitted for brevity. We next consider the other constraints in (7).

Considering the first constraint in (7) with link (i, j), we have that

$$\sum_{u \in \mathcal{H}} H_u \left[\frac{1}{|\Phi|} \sum_{\phi \in \Phi} \beta_{\phi(i)\phi(j)}^{\phi(u)*} + \frac{1}{|\Phi|} \sum_{\phi \in \Phi} \gamma_{\phi(i)\phi(j)}^{\phi(u)*} \right]$$
$$= \frac{1}{|\Phi|} \sum_{\phi \in \Phi} \sum_{u \in \mathcal{H}} H_{\phi(u)} \left(\beta_{\phi(i)\phi(j)}^{\phi(u)*} + \gamma_{\phi(i)\phi(j)}^{\phi(u)*} \right) \leq \eta^*$$

The first equality uses the node capacity property in Definition 1. The last inequality uses the fact that the inner summation is at most η^* since the optimal solution satisfies the constraint with link $(\phi(i), \phi(j))$. Therefore, the first constraint in (7) is satisfied.

Considering the second constraint in (7) with commodity (u, v) and link (i, j), we have that

$$\frac{1}{C_{ij} |\Phi|} \sum_{\phi \in \Phi} f_{\phi(i)\phi(j)}^{\phi(u)\phi(v)*} - \frac{1}{|\Phi|} \sum_{\phi \in \Phi} \beta_{\phi(i)\phi(j)}^{\phi(u)*} - \frac{1}{|\Phi|} \sum_{\phi \in \Phi} \gamma_{\phi(i)\phi(j)}^{\phi(v)*} \\
= \frac{1}{|\Phi|} \sum_{\phi \in \Phi} \left[\frac{f_{\phi(i)\phi(j)}^{\phi(u)\phi(v)*}}{C_{\phi(i)\phi(j)}} - \beta_{\phi(i)\phi(j)}^{\phi(u)*} - \gamma_{\phi(i)\phi(j)}^{\phi(v)*} \right] \leq 0.$$

The first equality uses the link capacity property in Definition 1. The last inequality uses the fact that the expression in the summation is non-positive since the optimal solution satisfies the constraint with commodity $(\phi(u), \phi(v))$ and link $(\phi(i), \phi(j))$. Therefore, the second constraint in (7) is satisfied. Altogether, the solution is feasible and optimal.

Finally, we show the solution satisfies the automorphisminvariant property. We first consider $\hat{\beta}_{ij}^{u}$ and any $\phi \in \Phi$:

$$\hat{\beta}_{\phi(i)\phi(j)}^{\phi(u)} = \frac{1}{|\Phi|} \sum_{\phi' \in \Phi} \beta_{\phi'(\phi(i))\phi'(\phi(j))}^{\phi'(\phi(u))*} = \frac{1}{|\Phi|} \sum_{\phi'' \in \Phi} \beta_{\phi''(i)\phi''(j)}^{\phi''(u)*} = \hat{\beta}_{ij}^{u}.$$

The second equality used the fact that the set of all automorphism mapping is a group and the summation is over the entire set. Therefore, every variable $\hat{\beta}_{ij}^{u}$ is automorphism-invariant. Similar arguments prove the automorphism-invariant property of variables \hat{f}_{ij}^{uv} and $\hat{\gamma}_{ij}^{u}$ and are omitted. Thus, the solution is automorphism-invariant. This proves the theorem.

The implication of Theorem 2 is that the linear program in (7) always has an optimal solution whose variables form groups. Every variable in each group takes the same value, i.e., $\hat{\beta}_{ij}^u = \beta_{\phi(i)\phi(j)}^{\phi(u)}$ for all $\phi \in \Phi$. We use this insight to formulate a new linear program targeting the automorphisminvariant optimal solution to improve scalability.

C. Representative variables

We first identify how variables $[\beta_{ij}^u], [\gamma_{ij}^u], [f_{ij}^{uv}]$ of the linear program in (7) form groups, so the variables in each group can be represented by a representative variable. The challenge of this identification is its combinatorial nature.

We observe from Theorem 2 that the variables $[\beta_{ij}^u]$ and $[\gamma_{ij}^u]$ share the same group relations as they share the same index set, $\mathcal{H} \times \mathcal{L}$. We therefore develop an efficient algorithm to group the variables based on the generator set $\hat{\Phi}$ as summarized in Algorithm 2. The algorithm searches over the index set. It takes an index from the set and utilizes the generator set to find all indices sharing the same group. Once a group is formed, the algorithm takes an unvisited index and continues the search process until all groups are formed. The time-complexity of Algorithm 1 is $O(|\mathcal{H}| |\mathcal{L}| |\hat{\Phi}|)$, since every index is visited once and $|\hat{\Phi}|$ indices are searched over per visited index. Using the generator set is more efficient than the entire automorphism set, which is exponentially large.

The algorithm outputs the representative index set \mathcal{G} and the dictionary ω containing automorphisms that can map each variable to its representative. In particular, let $\hat{\beta}_{ij}^{u}$ and $\hat{\gamma}_{ij}^{u}$ be representative variables for every $(u, i, j) \in \hat{\mathcal{G}}$. They represent variables $[\beta_{ij}^{u}]$ and $[\gamma_{ij}^{u}]$ of the linear program in (7) as follows:

$$\begin{split} \beta_{ij}^{u} & \xrightarrow{\text{represented by}} \varphi \left[\beta_{ij}^{u} \right] = \hat{\beta}_{\phi(i)\phi(j)}^{\phi(u)} \quad \text{where } \phi = \omega \left[u, i, j \right] \\ \gamma_{ij}^{u} & \xrightarrow{\text{represented by}} \varphi \left[\gamma_{ij}^{u} \right] = \hat{\gamma}_{\phi(i)\phi(j)}^{\phi(u)} \quad \text{where } \phi = \omega \left[u, i, j \right] \end{split}$$

for every $(u, i, j) \in \mathcal{H} \times \mathcal{L}$. We use $\varphi[x]$ to denote the representative of x.

Algorithm 2: Identification of $[\hat{\beta}_{ij}^u]$ and $[\hat{\gamma}_{ij}^u]$
Initialize empty sets $\mathcal{Q}, \mathcal{Z}, \hat{\mathcal{G}}$
Initialize dictionaries D, ω
for $(u, i, j) \in \mathcal{H} imes \mathcal{L}$ do
if $(u, i, j) \in \mathcal{Z}$ then
$\hat{\mathcal{G}} \leftarrow \hat{\mathcal{G}} \cup \{(u, i, j)\}$
$D[u, i, j] \leftarrow \phi_{\text{identity}}$
$\mathcal{Q} \leftarrow \mathcal{Q} \cup \{(u, i, j)\}$
while Q is not empty do
Pop (a, b, c) from Q
$\mathcal{Z} \leftarrow \mathcal{Z} \cup \{(a, b, c)\}$
for $\phi\in\hat{\Phi}$ do
if $(\phi(a), \phi(b), \phi(c)) \notin \mathcal{Z}$ then
$\left \begin{array}{c} \mathcal{Q} \leftarrow \mathcal{Q} \cup \left\{ \left(\phi\left(a \right), \phi\left(b \right), \phi\left(c \right) \right) \right\} \right.$
$D[\phi(a), \phi(b), \phi(c)] \leftarrow \phi(D[a, b, c])$
for $(u, i, j) \in \mathcal{H} imes \mathcal{L}$ do

$$_ \omega [u, i, j] \leftarrow (D[u, i, j])^-$$

return representative index set $\hat{\mathcal{G}}$ and dictionary ω

Algorithm 3: Construction of index set $\hat{\mathcal{M}}$ Initialize empty sets $\mathcal{Z}, \hat{\mathcal{M}}$ for $(i, j) \in \mathcal{L}$ do Let $e = (a_{uij}, b_{uij})_{(u,i,j)\in\hat{\mathcal{G}}}$ where a_{uij} and b_{uij} are respectively the counts of representative $\hat{\beta}_{ij}^{u}$ and $\hat{\gamma}_{ij}^{u}$ in $\sum_{n \in \mathcal{H}} H_n \left(\hat{\beta}_{ij}^n + \hat{\gamma}_{ij}^n \right)$ if $e \in \mathcal{Z}$ then \perp continue $\hat{\mathcal{M}} \leftarrow \hat{\mathcal{M}} \cup \{(i, j)\}$ $\mathcal{Z} \leftarrow \mathcal{Z} \cup \{e\}$ return representative constraint indices $\hat{\mathcal{M}}$

For the share variables $[f_{ij}^{uv}]$, their groups can be obtained from Algorithms 2 and 3 in [12], whose multi-commodity formulation includes a related conservation constraint similar to ours in (2). Algorithm 2 in [12] outputs the set of representative commodities, \hat{C} , and a dictionary π containing automorphisms that can map each commodity to its representative commodity. For each representative commodity $(u, v) \in \hat{C}$, Algorithm 3 in [12] outputs the set of representative links of share variables, $\hat{\mathcal{L}}^{uv}$, and a dictionary σ^{uv} containing automorphisms that can map each share variable to its representative. In particular, let \hat{f}_{ij}^{uv} be a representative share variable for every $(u, v) \in \hat{C}$ and every $(i, j) \in \hat{\mathcal{L}}^{uv}$. They represent shares $[f_{ij}^{uv}]$ of the linear program in (7) as follows:

$$f_{ij}^{uv} \xrightarrow{\text{represented by}} \varphi\left[f_{ij}^{uv}\right] = \hat{f}_{\phi'(\phi(i))\phi'(\phi(j))}^{\phi(u)\phi(v)}$$

where $\phi = \pi [u, v]$, and $\phi' = \sigma^{\phi(u)\phi(v)}[i, j]$ for every $(u, v, i, j) \in \mathcal{C} \times \mathcal{L}$.

These representatives $[\hat{\beta}_{ij}^{u}], [\hat{\gamma}_{ij}^{u}], [\hat{f}_{ij}^{uv}]$ significantly reduce the variables of the linear program in (7), resulting in a scalable linear program.

D. Automorphism-invariant formulation

Finally, we formulate the scalable linear program for large networks as follows:

Minimize η

Subject to
$$\sum_{\substack{u \in \mathcal{H} \\ \hat{f}_{ij}^{uv} \\ \overline{C}_{ij}}} H_u \left(\varphi \left[\beta_{ij}^u \right] + \varphi \left[\gamma_{ij}^u \right] \right) \leq \eta \;, \forall (i,j) \in \hat{\mathcal{M}}$$
$$\frac{\hat{f}_{ij}^{uv}}{C_{ij}} - \varphi \left[\beta_{ij}^u \right] - \varphi \left[\gamma_{ij}^v \right] \leq 0$$
$$; \forall (u,v) \in \hat{\mathcal{C}}, \forall (i,j) \in \hat{\mathcal{L}}^{uv}$$
$$\hat{\beta}_{ij}^u \in \mathbb{R}_+, \; \hat{\gamma}_{ij}^u \in \mathbb{R}_+, \forall (u,i,j) \in \hat{\mathcal{G}}$$
$$[\hat{f}_{ij}^{uv}] \in \hat{\mathcal{F}}, \qquad (8)$$

where the set $\hat{\mathcal{F}}$ is defined in (9).

$$\begin{aligned} \mathcal{F} &= \\ \left\{ \begin{array}{l} \sum_{j \in \mathcal{O}(i)} \hat{f}_{ij}^{uv} - \sum_{j \in \mathcal{I}(i)} \hat{f}_{ji}^{uv} = \mathbb{I}[i = u] - \mathbb{I}[i = v] \\ &, \forall (u, v) \in \hat{\mathcal{C}}, \forall i \in \mathcal{N} \\ \hat{f}_{ij}^{uv} = 0 &, \forall (u, v) \in \hat{\mathcal{C}}, \forall (i, j) \in \hat{\mathcal{L}}^{uv} \cap (\mathcal{N} \times \mathcal{P}_{-v}) \\ \hat{f}_{iu}^{uv} = \hat{f}_{vi}^{uv} = 0, \forall (u, v) \in \hat{\mathcal{C}}, \forall (i, u), (v, i) \in \hat{\mathcal{L}}^{uv} \\ \hat{f}_{ij}^{uv} \in \mathbb{R}_{+} &, \forall (u, v) \in \hat{\mathcal{C}}, \forall (i, j) \in \hat{\mathcal{L}}^{uv} \end{aligned} \right\}$$

The set $\hat{\mathcal{M}}$ in (8) is constructed by Algorithm 3 to remove unnecessary constraints, causing intractability for large networks.

The scalable linear program in (8) designs an optimal oblivious routing solution in polynomial time for large networks. It is faster and more scalable than the state-of-the-art technique [12] as shown in Section VI. The next section describes how an optimal solution is further optimized for practical deployment.

V. COMPACT FORWARDING RULES

An optimal oblivious routing solution, obtained from the scalable formulation in (8), could be distributively deployed on switches in a datacenter network. Every switch decides how



Fig. 5. Optimal oblivious routing solution of commodities (0, 4) and (0, 10), which have the same representative, is presented. The black arrows represent similar split weights that can be grouped at each corresponding node.

traffic is split over next-hop switches for each commodity.² In particular, switch i splits the traffic of commodity (u, v) to next-hop switch j over link (i, j) with weight

$$w_{ij}^{uv} = \frac{f_{ij}^{uv}}{\sum_{j' \in \mathcal{O}(i)} f_{ij'}^{uv}} \quad , \forall (u,v) \in \mathcal{C}, \forall i \in \mathcal{N}, \forall j \in \mathcal{O}(i).$$

$$(10)$$

These weights could be configured into programmable switches in the form of forwarding rules [18], [27], [28]. However, a switch has limited memory for storing the rules from multiple commodities in a large datacenter network. This practical limitation could hinder the deployment of the optimal oblivious routing solution.

We observe that some rules, derived from the commodities having the same representative, are similar as shown in Figure 5. We use this insight to group the forwarding rules in order to reduce memory requirement. Algorithm 4 summarized our grouping method. For each node in a network, the commodities having the same representative are grouped by the similarity of split weights, i.e., $(i, j, \varphi \left[f_{ij}^{xy} \right])_{(i,j) \in \mathcal{O}(n)}$ (ignoring the common denominator in (10)). The commodities having similar split weights form a group, so one collection of forwarding rules is sufficient for these commodities. The algorithm outputs the collection of grouped rules, $\{W_n\}_{n \in \mathcal{N}}$. For node n, each collection of grouped rules is stored in the dictionary W_n whose key e represents a collection of split weights and the value $W_n[e]$ is the set of commodities using the weights. Note that Algorithm 4 is highly parallelizable as the grouping process can be parallelized for each node.

In short, Algorithm 4 utilizes the repeated structures in the optimal oblivious routing solution, which is automorphism invariant, to reduce memory requirement for the deployment of forwarding rules in real-world switches. In addition, if the reduced requirement exceeds the available memory of a switch, an approximation technique [18] could be applied to our grouped rules to trade-off between optimality and available memory. In other words, our grouping method circumvents the unnecessary trade-off when it is avoidable.

VI. EVALUATION

Our scalable linear program in (8) is evaluated over various datacenter network topologies and sizes. Its scalability is eval-

Algorithm 4: Forward-rule grouping

Initialize empty set QInitialize dictionary $\{W_n\}_{n \in \mathcal{N}}$ for $n \in \mathcal{N}$ do $Q \leftarrow Q \cup \{n\}$ for $(u, v) \in \hat{C}$ do for (x, y) represented by (u, v) do Let $e = (i, j, \varphi [f_{ij}^{xy}])_{(i,j)\in\mathcal{O}(n)}$ if $e \notin W_n$ then $\bigcup W_n[e] \leftarrow \emptyset$ $W_n[e] \leftarrow W_n[e] \cup \{(x, y)\}$ return Grouped forwarding rules $\{W_n\}_{n \in \mathcal{N}}$



Fig. 6. The optimal oblivious routing solution of the network in Figure 2. The four commodities correspond to all combinations of node types.

uated in terms of computation times and problem sizes. The efficiency of our grouping method in Algorithm 4 is evaluated for the same topologies. Ultimately, we show the applicability of our work for an existing server-centric topology.

Every evaluation is executed on a commodity computer with an Intel Core i9-12900K processor and 128GB memory. All linear programs are solved by Gurobi [29]. We use nauty [26] to compute generator sets.

A. Topology setting and brief background

We motivate the topologies used in our evaluation as follows. FatClique is designed for low-cost manageability, regarding topology deployment and expansion [2]. SlimFly focuses on throughput performance by the use of low diameter graphs with high-radix switches [9]. BCube aims for shippingcontainer-based datacenters [10]. FatClique and SlimFly follow the server-switch architecture, while BCube belongs to the server-centric architecture.

These topologies are constructed as follows. For FatClique, we set the numbers of switches in a sub-block, sub-blocks in a block, and blocks to an identical value. We vary this value from 2 to 12. For SlimFly, we use its provided topologies with the number of network radices ranging from 5 to 43. For BCube, each topology is built from 4-port switches, and we vary the number of levels of switches from 2 to 5. The maximum number of nodes in the topologies generated by these settings is 2304.

B. Correctness of optimal solutions

In every evaluation, we confirm the correctness of the optimal solutions obtained from (8) by comparing them with the solutions from the state-of-the-art technique in [12]. When

 $^{^{2}}$ TCP reordering effect can be avoided by splitting traffic based on flows instead of packets.



Fig. 7. The optimization time at different sizes of FatClique, SlimFly and BCube. The maximum times of the scalable formulation are 0.008 sec. for FatClique, 2.97 min. for SlimFly, and 0.026 sec. for BCube. Note that SlimFly topologies beyond 50 nodes cannot be solved by [16] due to insufficient computing memory.

the technique fails to compute solutions, we adopt another technique in [30] to validate our solutions.

Moreover, as shown in Figure 6, the scalable linear program in (8) handles routing-incapable nodes correctly for the simple network in Figure 2. Every routing-incapable node that is unrelated to a considered commodity does not involve in the commodity's routing.

C. Scalability in terms of computation time

We vary the sizes of FatClique, SlimFly, and BCube. We record the optimization times of our scalable formulation in (8), the state-of-the-art technique in [12], and the other linear program in [16]. The limit of the optimization time is set to 24 hours. The results are plotted in Figure 7. Our work takes much lesser time to find optimal routing solutions than the other techniques for all topologies and all sizes. The work in [12] scales well for FatClique and BCube but fails to obtain optimal routing solutions within the time limit for SlimFly beyond 98 nodes. The linear program in [16] cannot scale beyond 112 nodes for all considered topologies due to insufficient computing memory.

D. Scalability in terms of variables and constraints

Figure 8 shows the numbers of variables and constraints at different sizes of FatClique from 8 to 1728 nodes, which follows the same evaluation in [12]. Our scalable formulation in (8) is much leaner than the other techniques regarding the numbers of variables and constraints. While the linear program in [16] can obtain an optimal routing solution in polynomial time, its formulation size grows exponentially as the topology size increases, resulting in the insufficient memory issue in Section VI-C. Since the state-of-the-art technique in [12] iteratively solves two linear programs, we only count the total numbers of variables and constraints from the two programs at the first iteration without additional constraints from later iterations. Yet, its formulation size is still larger than ours.

E. Reduction of forwarding rules

The efficiency of the forwarding rule grouping in Algorithm 4 is measured by the percentage of space saving, which is the proportion of rule reduction to non-grouped rules. Figure 9 shows the space saving at various sizes of FatClique, SlimFly, and BCube. Our grouping method reduces more than



Fig. 8. The numbers of variables and constraints at various sizes of FatClique. For the largest FatClique, the numbers of variables and constraints in the scalable formulation are 160 and 5326 respectively.



Fig. 9. The space saving (percentage) at different sizes of FatClique, SlimFly, and BCube. Each mean value is computed from all nodes in a topology. The maximum and minimum values are represented by horizontal bars.

90% of the non-grouped forwarding rules under FatClique with no less than 216 nodes and under BCube with no less than 112 nodes. The space saving for SlimFly highly depends on topology configuration.

F. Possible application for BCube

BCube employs dynamic source routing to utilize multipath capacity. Each source selects the best-quality path from its candidate paths, based on the current maximum available bandwidth. This approach constantly measures available bandwidth, introduces complexity, and may need specialized hardware and network stack. Instead, one could employ the oblivious routing approach on BCube for simpler production.

Table I shows the results of our optimal routing solution and equal split, which is equivalent to a uniform selection of paths in BCube's dynamic routing. The optimal solution gains $1.8 \times -6.7 \times$ congestion improvement over the equal split.

VII. CONCLUSION

This paper proposes the polynomial-time process for designing optimal compact oblivious routing for datacenter networks. In the process, an optimal oblivious routing solution is designed by solving a scalable linear program, derived from the transformation of a robust optimization problem and the exploitation of repeated network structures. After obtaining an optimal routing solution, the process compacts the forwarding rules, converted from the optimal solution, in order to reduce memory requirement for real-world deployment.

Potential future work includes topology asymmetry, network failure, and prior traffic distribution.

The authors have provided public access to their code at https://github.com/NDS-VISTEC/DCN-Oblivious-Routing.

 TABLE I

 The maximum congestion ratio in BCube with oblivious routing

BCube	Maximum congestion ratio		
#Nodes	This work	Equal split	Improvement
24	2.50	4.50	1.80x
112	4.00	11.49	2.87x
512	5.50	21.50	3.91x
2304	6.97	47.19	6.77x

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